

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)

Search Results -

Term	Documents
STORE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	430873
STORES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	445867
BUFFER.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	490861
BUFFERS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	124648
ENTRIES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	56804
ENTRY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	349738
ENTRYS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	62
MULTIPLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	973307
MULTIPLES.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	31917
MULTI.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	661615
MULTIS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	42
(STORE NEAR1 BUFFER NEAR5 ENTRIES AND (MULTIPLE OR MULTI OR PLURAL\$5) NEAR5 MATCH AND LOAD).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	13

There are more results than shown above. [Click here to view the entire set.](#)

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L4

[Refine Search](#)[Recall Text](#)[Clear](#)

Search History

DATE: Monday, September 09, 2002 [Printable Copy](#) [Create Case](#)

WEST[Generate Collection](#)[Print](#)

Search Results - Record(s) 1 through 3 of 3 returned.

☐ 1. Document ID: US 6223320 B1

L5: Entry 1 of 3

File: USPT

Apr 24, 2001

US-PAT-NO: 6223320

DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations

Full	Title	1.1	CV.1	LS.1	EP.1	QQ.1	11.1
NAW.1							

☐ 2. Document ID: US 5887183 A

L5: Entry 2 of 3

File: USPT

Mar 23, 1999

US-PAT-NO: 5887183

DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors in a plurality of modes

Full	Title	Citation	Front	Review	Classification	EP.2	QQ.2	11.2
NAW.2								

☐ 3. Document ID: US 5764997 A

L5: Entry 3 of 3

File: USPT

Jun 9, 1998

US-PAT-NO: 5764997

DOCUMENT-IDENTIFIER: US 5764997 A

TITLE: System for generating interrupt requests from either side of an inter-chip bus

Full	Title	Citation	Front	Review	Classification	Date	EP.3	Sequences	11.3	KMC
NAW.3										

[Generate Collection](#)[Print](#)

Term	Documents
TARGET.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	414203
TARGETS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	63779
VECTOR\$	0
VECTOR.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	199003
VECTORA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	21
VECTORAB.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
VECTORABILITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
VECTORABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	193
VECTORADD.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	6
VECTORADDS.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
"VECTORADD[".DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	3
(L3 AND TARGET NEAR1 VECTOR\$).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	3

[There are more results than shown above. Click here to view the entire set.](#)

Display Format: [Change Format](#)

[Previous Page](#)

[Next Page](#)

Print

☐ 1. Document ID: US 20020116595 A1

Aug 22, 2002

DOCUMENT-IDENTIFIER: US 20020116595 A1

PUBLICATION-DATE: August 22, 2002

INVENTOR - INFORMATION:

RULE-47

US

US-CL-CURRENT: 712/22

[illegible]

☐ 2. Document ID: US 6332186 B1

Dec 18, 2001

DOCUMENT-IDENTIFIER: US 6332186 B1

TITLE: Vector register addressing

[illegible]

☐ 3. Document ID: US 6317819 B1

Nov 13, 2001

DOCUMENT-IDENTIFIER: US 6317819 B1

TITLE: Digital signal processor containing scalar processor and a plurality of vector processors operating from a single instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KIMC

☐ 4. Document ID: US 6304963 B1

L6: Entry 4 of 39

File: USPT

Oct 16, 2001

US-PAT-NO: 6304963

DOCUMENT-IDENTIFIER: US 6304963 B1

TITLE: Handling exceptions occuring during processing of vector instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 5. Document ID: US 6282634 B1

L6: Entry 5 of 39

File: USPT

Aug 28, 2001

US-PAT-NO: 6282634

DOCUMENT-IDENTIFIER: US 6282634 B1

TITLE: Apparatus and method for processing data having a mixed vector/scalar register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 6. Document ID: US 6266758 B1

L6: Entry 6 of 39

File: USPT

Jul 24, 2001

US-PAT-NO: 6266758

DOCUMENT-IDENTIFIER: US 6266758 B1

TITLE: Alignment and ordering of vector elements for single instruction multiple data processing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 7. Document ID: US 6247113 B1

L6: Entry 7 of 39

File: USPT

Jun 12, 2001

US-PAT-NO: 6247113

DOCUMENT-IDENTIFIER: US 6247113 B1

TITLE: Coprocessor opcode division by data type

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 8. Document ID: US 6223320 B1

L6: Entry 8 of 39

File: USPT

Apr 24, 2001

US-PAT-NO: 6223320

DOCUMENT-IDENTIFIER: US 6223320 B1

TITLE: Efficient CRC generation utilizing parallel table lookup operations

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 9. Document ID: US 6189094 B1

L6: Entry 9 of 39

File: USPT

Feb 13, 2001

US-PAT-NO: 6189094

DOCUMENT-IDENTIFIER: US 6189094 B1

TITLE: Recirculating register file

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 10. Document ID: US 6170001 B1

L6: Entry 10 of 39

File: USPT

Jan 2, 2001

US-PAT-NO: 6170001

DOCUMENT-IDENTIFIER: US 6170001 B1

TITLE: System for transferring format data from format register to memory wherein format data indicating the distribution of single or double precision data type in the register bank

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 11. Document ID: US 6088783 A

L6: Entry 11 of 39

File: USPT

Jul 11, 2000

US-PAT-NO: 6088783

DOCUMENT-IDENTIFIER: US 6088783 A

TITLE: DPS having a plurality of like processors controlled in parallel by an instruction word, and a control processor also controlled by the instruction word

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 12. Document ID: US 6085275 A

L6: Entry 12 of 39

File: USPT

Jul 4, 2000

US-PAT-NO: 6085275

DOCUMENT-IDENTIFIER: US 6085275 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 13. Document ID: US 6058465 A

L6: Entry 13 of 39

File: USPT

May 2, 2000

US-PAT-NO: 6058465

DOCUMENT-IDENTIFIER: US 6058465 A

TITLE: Single-instruction-multiple-data processing in a multimedia signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 14. Document ID: US 5961628 A

L6: Entry 14 of 39

File: USPT

Oct 5, 1999

US-PAT-NO: 5961628

DOCUMENT-IDENTIFIER: US 5961628 A

TITLE: Load and store unit for a vector processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 15. Document ID: US 5887183 A

L6: Entry 15 of 39

File: USPT

Mar 23, 1999

US-PAT-NO: 5887183

DOCUMENT-IDENTIFIER: US 5887183 A

TITLE: Method and system in a data processing system for loading and storing vectors in a plurality of modes

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 16. Document ID: US 5850227 A

L6: Entry 16 of 39

File: USPT

Dec 15, 1998

US-PAT-NO: 5850227

DOCUMENT-IDENTIFIER: US 5850227 A

TITLE: Bit map stretching using operand routing and operation selective multimedia extension unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 17. Document ID: US 5838984 A

L6: Entry 17 of 39

File: USPT

Nov 17, 1998

US-PAT-NO: 5838984

DOCUMENT-IDENTIFIER: US 5838984 A

TITLE: Single-instruction-multiple-data processing using multiple banks of vector registers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 18. Document ID: US 5825677 A

L6: Entry 18 of 39

File: USPT

Oct 20, 1998

US-PAT-NO: 5825677

DOCUMENT-IDENTIFIER: US 5825677 A

TITLE: Numerically intensive computer accelerator

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 19. Document ID: US 5822606 A

L6: Entry 19 of 39

File: USPT

Oct 13, 1998

US-PAT-NO: 5822606

DOCUMENT-IDENTIFIER: US 5822606 A

TITLE: DSP having a plurality of like processors controlled in parallel by an instruction word, and a control processor also controlled by the instruction word

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 20. Document ID: US 5805874 A

L6: Entry 20 of 39

File: USPT

Sep 8, 1998

US-PAT-NO: 5805874

DOCUMENT-IDENTIFIER: US 5805874 A

TITLE: Method and apparatus for performing a vector skip instruction in a data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

[Generate Collection](#)[Print](#)

Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

[There are more results than shown above. Click here to view the entire set.](#)

Display Format:

-

[Change Format](#)[Previous Page](#)[Next Page](#)

WEST[Generate Collection](#)[Print](#)

Search Results - Record(s) 21 through 39 of 39 returned.

☐ 21. Document ID: US 5799163 A

L6: Entry 21 of 39

File: USPT

Aug 25, 1998

US-PAT-NO: 5799163

DOCUMENT-IDENTIFIER: US 5799163 A

TITLE: Opportunistic operand forwarding to minimize register file read ports

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 22. Document ID: US 5754805 A

L6: Entry 22 of 39

File: USPT

May 19, 1998

US-PAT-NO: 5754805

DOCUMENT-IDENTIFIER: US 5754805 A

TITLE: Instruction in a data processing system utilizing extension bits and method therefor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 23. Document ID: US 5752074 A

L6: Entry 23 of 39

File: USPT

May 12, 1998

US-PAT-NO: 5752074

DOCUMENT-IDENTIFIER: US 5752074 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 24. Document ID: US 5742786 A

L6: Entry 24 of 39

File: USPT

Apr 21, 1998

US-PAT-NO: 5742786

DOCUMENT-IDENTIFIER: US 5742786 A

TITLE: Method and apparatus for storing vector data in multiple non-consecutive locations in a data processor using a mask value

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 25. Document ID: US 5737586 A

L6: Entry 25 of 39

File: USPT

Apr 7, 1998

US-PAT-NO: 5737586

DOCUMENT-IDENTIFIER: US 5737586 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 26. Document ID: US 5734879 A

L6: Entry 26 of 39

File: USPT

Mar 31, 1998

US-PAT-NO: 5734879

DOCUMENT-IDENTIFIER: US 5734879 A

TITLE: Saturation instruction in a data processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 27. Document ID: US 5717947 A

L6: Entry 27 of 39

File: USPT

Feb 10, 1998

US-PAT-NO: 5717947

DOCUMENT-IDENTIFIER: US 5717947 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 28. Document ID: US 5706488 A

L6: Entry 28 of 39

File: USPT

Jan 6, 1998

US-PAT-NO: 5706488

DOCUMENT-IDENTIFIER: US 5706488 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 29. Document ID: US 5673407 A

L6: Entry 29 of 39

File: USPT

Sep 30, 1997

US-PAT-NO: 5673407

DOCUMENT-IDENTIFIER: US 5673407 A

TITLE: Data processor having capability to perform both floating point operations and memory access in response to a single instruction

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 30. Document ID: US 5664134 A

L6: Entry 30 of 39

File: USPT

Sep 2, 1997

US-PAT-NO: 5664134

DOCUMENT-IDENTIFIER: US 5664134 A

TITLE: Data processor for performing a comparison instruction using selective enablement and wired boolean logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 31. Document ID: US 5600846 A

L6: Entry 31 of 39

File: USPT

Feb 4, 1997

US-PAT-NO: 5600846

DOCUMENT-IDENTIFIER: US 5600846 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 32. Document ID: US 5598571 A

L6: Entry 32 of 39

File: USPT

Jan 28, 1997

US-PAT-NO: 5598571

DOCUMENT-IDENTIFIER: US 5598571 A

TITLE: Data processor for conditionally modifying extension bits in response to data processing instruction execution

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 33. Document ID: US 5572689 A

L6: Entry 33 of 39

File: USPT

Nov 5, 1996

US-PAT-NO: 5572689

DOCUMENT-IDENTIFIER: US 5572689 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMMC

☐ 34. Document ID: US 5559973 A

L6: Entry 34 of 39

File: USPT

Sep 24, 1996

US-PAT-NO: 5559973

DOCUMENT-IDENTIFIER: US 5559973 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMMC

☐ 35. Document ID: US 5548768 A

L6: Entry 35 of 39

File: USPT

Aug 20, 1996

US-PAT-NO: 5548768

DOCUMENT-IDENTIFIER: US 5548768 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMMC

☐ 36. Document ID: US 5537562 A

L6: Entry 36 of 39

File: USPT

Jul 16, 1996

US-PAT-NO: 5537562

DOCUMENT-IDENTIFIER: US 5537562 A

TITLE: Data processing system and method thereof

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMMC

☐ 37. Document ID: US 5487022 A

L6: Entry 37 of 39

File: USPT

Jan 23, 1996

US-PAT-NO: 5487022

DOCUMENT-IDENTIFIER: US 5487022 A

TITLE: Normalization method for floating point numbers

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 38. Document ID: US 4945479 A

L6: Entry 38 of 39

File: USPT

Jul 31, 1990

US-PAT-NO: 4945479

DOCUMENT-IDENTIFIER: US 4945479 A

TITLE: Tightly coupled scientific processing system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 39. Document ID: US 4873630 A

L6: Entry 39 of 39

File: USPT

Oct 10, 1989

US-PAT-NO: 4873630

DOCUMENT-IDENTIFIER: US 4873630 A

TITLE: Scientific processor to support a host processor referencing common memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

[Generate Collection](#)[Print](#)

Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

There are more results than shown above. [Click here to view the entire set.](#)

Display Format:

[Previous Page](#)

[Next Page](#)

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show 8 Numbers](#)[Edit 8 Numbers](#)[Preferences](#)[Cases](#)

Search Results -

Term	Documents
ALIGN\$6	0
ALIGN.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	227813
ALIGNA.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	4
ALIGNABAL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABEL.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABIE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABILTY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	2
ALIGNABLE.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	13259
ALIGNABLITY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	1
ALIGNABLY.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	248
(L4 AND ALIGN\$6).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	39

There are more results than shown above. [Click here to view the entire set.](#)

Database:

US Patents Full-Text Database
US Pre-Grant Publication Full-Text Database
JPO Abstracts Database
EPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L6

[Refine Search](#)[Recall Text](#)[Clear](#)

Search History

DATE: Tuesday, September 10, 2002 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L6</u>	L4 and align\$6	39	<u>L6</u>
<u>L5</u>	L3 and target near1 vector\$	3	<u>L5</u>
<u>L4</u>	L3 and target	60	<u>L4</u>
<u>L3</u>	(simd or single near1 data) and vector\$ near6 (destination or source) near5 register\$	98	<u>L3</u>
<u>L2</u>	(simd or single near1 data) and vector\$ near6 (destination or source)	316	<u>L2</u>

DB=USPT; PLUR=YES; OP=OR

<u>L1</u>	5671401.pn. or 6035316.pn.	2	<u>L1</u>
-----------	----------------------------	---	-----------

END OF SEARCH HISTORY

IEEE HOME | SEARCH IEEE | SHOP | WEB ACCOUNT | CONTACT IEEE

Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore™
RELEASE 1.4Welcome
United States Patent and Trademark Office[Help](#) [FAQ](#) [Terms](#) [IEEE Peer](#) [Quick Links](#)

» Search

Welcome to IEEE Xplore™

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account

 Print FormatYour search matched **7** of **795365** documents.Results are shown **25** to a page, sorted by **publication year** in **descending** order.

You may refine your search by editing the current search expression or entering a new one the text

Then click **Search Again**.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**

1 Exploiting a new level of DLP in multimedia applications*Corbal, J.; Valero, M.; Espasa, R.*

Microarchitecture, 1999. MICRO-32. Proceedings. 32nd Annual International Symposium on , 1999

Page(s): 72 -79

[\[Abstract\]](#) [\[PDF Full-Text \(80 KB\)\]](#) **CNF**

2 Effective SIMD code generation for the high-level declarative data-language 8 1/2*De Vito, D.; Michel, O.*

EUROMICRO 96. Beyond 2000: Hardware and Software Design Strategies., Proceedings of the 22nd EUROMICRO Conference , 1995

Page(s): 114 -119

[\[Abstract\]](#) [\[PDF Full-Text \(436 KB\)\]](#) **CNF**

3 Data-parallel functional programming*VanderWiel, S.; Davis, J.A.*

Parallel Processing Symposium, 1993., Proceedings of Seventh International Page(s): 602 -608

[\[Abstract\]](#) [\[PDF Full-Text \(560 KB\)\]](#) **CNF**

4 A compiler for a massively parallel distributed memory MIMD computer
Sabot, G.

Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on the Page(s): 12 -20

[\[Abstract\]](#) [\[PDF Full-Text \(840 KB\)\]](#) **CNF**

5 Connection Machine model CM-5 system overview*Palmer, J.; Steele, G.L., Jr.*

Frontiers of Massively Parallel Computation, 1992., Fourth Symposium on th

Page(s): 474 -483

[\[Abstract\]](#) [\[PDF Full-Text \(836 KB\)\]](#) **CNF****6 Algorithmic mapping of neural network models onto parallel SIMD***Lin, W.-M.; Prasanna, V.K.; Przytula, K.W.*

Computers, IEEE Transactions on , Volume: 40 Issue: 12 , Dec. 1991

Page(s): 1390 -1401

[\[Abstract\]](#) [\[PDF Full-Text \(980 KB\)\]](#) **JNL****7 Experience with moment-method software of vector processors***Excell, P.S.; Armanious, A.F.*Large Scale Programs for Use in Antenna Performance Prediction, IEE Colloq
1989

Page(s): 3/1 -3/4

[\[Abstract\]](#) [\[PDF Full-Text \(188 KB\)\]](#) **CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)